# A hybrid method for the parallel computation of Green's functions 

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#### Abstract

Quantum transport models for nanodevices using the non-equilibrium Green's function method require the repeated calculation of the block tridiagonal part of the Green's and lesser Green's function matrices. This problem is related to the calculation of the inverse of a sparse matrix. Because of the large number of times this calculation needs to be performed, this is computationally very expensive even on supercomputers. The classical approach is based on recurrence formulas which cannot be efficiently parallelized. This practically prevents the solution of large problems with hundreds of thousands of atoms. We propose new recurrences for a general class of sparse matrices to calculate Green's and lesser Green's function matrices which extend formulas derived by Takahashi and others. We show that these recurrences may lead to a dramatically reduced computational cost because they only require computing a small number of entries of the inverse matrix. Then, we propose a parallelization strategy for block tridiagonal matrices which involves a combination of Schur complement calculations and cyclic reduction. It achieves good scalability even on problems of modest size.


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## 1. Introduction

Commercial electronic devices are rapidly approaching the scale where quantum mechanical effects are affecting the device characteristics [1]. For example, nano-transistors in the $10-30 \mathrm{~nm}$ range are strongly affected by quantum mechanical effects such as tunneling and leakage. To allow for continued scaling of silicon technology, new materials are introduced [2] and new nanomaterials such as nanowires [3] or carbon nanotubes [4] are currently being considered as wires or as active parts in future generations of transistors. Also, more exotic devices using DNA [5] or other small molecules [6] are currently under investigation for future use in electronic devices.

To model such devices, it is necessary to apply a quantum mechanical framework [2]. In a system where current flows from a large reservoir (source) to a large drain, the non-equilibrium Green's function approach is applicable [7-9]. This is

[^0]a very general framework which can be applied to all the systems mentioned above. This method involves the coupled solution of the Schrödinger equation for the quantum wave function and the Poisson equation for the electrostatic potential. The Schrödinger equation can be solved in multiple ways, including using a Kohn-Sham Hamiltonian. The main computational difficulty of this problem is the self-consistent solution of the Kohn-Sham Hamiltonian and the Poisson equation, which usually requires an iterative scheme with many iterations. The solution of this system requires the calculation of a Green's function which, once discretized, is often written in the matrix form:
\[

$$
\begin{equation*}
\mathbf{G} \stackrel{\text { def }}{=} \mathbf{A}^{-1}, \quad \text { where } \mathbf{A} \stackrel{\text { def }}{=} E \mathbf{S}^{0}-\mathbf{H}-\boldsymbol{\Sigma}^{\mathrm{L}}-\boldsymbol{\Sigma}^{\mathrm{R}} \tag{1}
\end{equation*}
$$

\]

where $E$ is an energy point (a scalar), $\mathbf{S}^{\mathbf{0}}$ is an overlap matrix, $\mathbf{H}$ is the Hamiltonian of the system, and $\boldsymbol{\Sigma}^{\mathrm{L}}$ and $\boldsymbol{\Sigma}^{\mathrm{R}}$ are the selfenergies. The Green's function matrix is computed at many energy points and this is computationally quite expensive even for small matrices. New nanodevices usually require a description at the atomic level; however, their size is large enough that we may have thousands or even millions of atoms in the active part of the device and thus the dimension of $\mathbf{A}$ becomes very large. In such cases, direct inversion of $\mathbf{A}$ becomes impractical, and the application of the non-equilibrium Green's function framework requires new efficient parallel algorithms that exploit the sparsity of $\mathbf{A}$. The focus of this paper is the derivation and benchmarking of a new parallel algorithm that efficiently calculates select entries in $\mathbf{G}$ and the lesser Green's function matrix

$$
\begin{equation*}
\mathbf{G}^{<} \stackrel{\text { def }}{=} \mathbf{G} \Gamma \mathbf{G}^{\dagger} \tag{2}
\end{equation*}
$$

where $\Gamma$ is at this point an arbitrary matrix. Assumptions regarding the non-zero entries of $\Gamma$ will be formulated later on. The lesser Green's function matrix is needed to account for non-equilibrium and scattering effects. The notations in this paper are indicated in Table 1.

In most cases not all entries in $\mathbf{G}$ and $\mathbf{G}^{<}$are needed. Usually only the diagonal entries of the matrix are required in the iterative process. This leads to a significant reduction in computational cost, which can be realized using a number of different methods [10]. Most of them are related to a technique developed by Takahashi et al. [11-13]. In this approach, a block LDU factorization of $\mathbf{A}$ is computed. Simple algebra shows that:

$$
\begin{align*}
\mathbf{G} & =\mathbf{D}^{-1} \mathbf{L}^{-1}+(\mathbf{I}-\mathbf{U}) \mathbf{G}  \tag{3}\\
& =\mathbf{U}^{-1} \mathbf{D}^{-1}+\mathbf{G}(\mathbf{I}-\mathbf{L}) \tag{4}
\end{align*}
$$

where $\mathbf{L}, \mathbf{U}$, and $\mathbf{D}$ correspond, respectively, to the lower block unit triangular, upper block unit triangular, and diagonal block LDU factorization of $\mathbf{A}$. The dense inverse $\mathbf{G}$ is treated conceptually as having a block structure based on that of $\mathbf{A}$.

For example, consider the first equation and $j>i$. The block entry $\mathbf{g}_{i j}$ resides above the block diagonal of the matrix and therefore $\left[\mathbf{D}^{-1} \mathbf{L}^{-1}\right]_{i j}=0$. The first equation can then be written as:

$$
\begin{equation*}
\mathbf{g}_{i j}=-\sum_{k>i} \mathbf{u}_{i k} \mathbf{g}_{k j} \tag{5}
\end{equation*}
$$

This allows computing the entry $\mathbf{g}_{i j}$ if the entries $\mathbf{g}_{k j}(k>i)$ below it are known. Two similar equations can be derived for the case $j<i$ and $j=i$ :

$$
\begin{align*}
& \mathbf{g}_{i j}=-\sum_{k>i} \mathbf{g}_{i k} \ell_{k j}  \tag{6}\\
& \mathbf{g}_{i i}=\mathbf{d}_{i i}^{-1}-\sum_{k>i} \mathbf{u}_{i k} \mathbf{g}_{k i} \tag{7}
\end{align*}
$$

This approach leads to fast backward recurrences, as shown in [12].
Table 1
Notations used in this paper.

| A | matrix |
| :---: | :---: |
| $\mathbf{a}_{i j}$ | block entry $(i, j)$ of matrix $\mathbf{A}$; in general, a matrix and its block entries are denoted by the same letter. A block is denoted with a bold lowercase letter while the matrix is denoted by a bold uppercase letter |
| $N$ | number of rows and columns in $\mathbf{A}$ |
| $n$ | number of blocks in matrix |
| d | block size |
| $\mathbf{A}^{T}$ | transpose matrix |
| $\mathbf{A}^{\dagger}$ | transpose conjugate matrix |
| $\mathbf{A}^{-\dagger}$ | transpose conjugate of the inverse matrix |
| L, D, U | block LDU factorization of A |
| $\mathbf{A}\left(i_{1}: i_{2}, j\right)$ | block column vector containing rows $i_{1}$ through $i_{2}$ in column $j$ of $\mathbf{A}$ |
| $\mathbf{A}\left(i, j_{1}: j_{2}\right)$ | block row vector containing columns $j_{1}$ through $j_{2}$ in row $i$ of $\mathbf{A}$ |
| $\mathbf{A}\left(i_{1}: i_{2}, j_{1}: j_{2}\right)$ | sub-matrix containing rows $i_{1}$ through $i_{2}$ and columns $j_{1}$ through $j_{2}$ of $\mathbf{A}$ |
| I | identity matrix |
| 0 | all-zero matrix |

The generalization of Takahashi's method to computing $\mathbf{G}^{<}$is not straightforward. In particular Eqs. (3) and (4) do not extend to this case. In this paper, we will provide a new way of deriving Eqs. (5)-(7). This derivation will then be extended to $\mathbf{G}^{<}$for which similar relations will be derived. These recurrences are most efficient when the sparsity graph of $\boldsymbol{\Gamma}$ (Eq. (2)) is a subset of the graph of A, i.e., $\gamma_{i j} \neq 0 \Rightarrow \mathbf{a}_{i j} \neq 0$.

For the purpose of computer implementation, an important distinction must be made to distinguish what we term 1D, 2D and 3D problems. In principle, all problems are 3 dimensional. However, if the mesh or device is elongated in one direction, say $z$, then the mesh can be split into "slices" along the $z$ direction. This gives rise to a matrix A with block tridiagonal structure. The problem is then termed 1 dimensional. Similarly, if the mesh is elongated in two directions, the matrix $\mathbf{A}$ assumes a block penta-diagonal form and the problem is called 2 dimensional.

This paper is organized as follows. We first describe general relations to compute Green's functions (Sections 2 and 3). These are applicable to meshes with arbitrary connectivity. In Section 4, we calculate the computational cost of these approaches for 1D and 2D Cartesian meshes and for discretizations involving nearest neighbor nodes only, e.g., a 3 point stencil in 1D or a 5 point stencil in 2D. We also compare the computational cost of this approach with a recently published method by Li and Darve (the FIND algorithm [14]).

In Section 5, a parallel implementation of the recurrences for 1 dimensional problems is proposed. The original algorithms by Takahashi [11] and Svizhenko [10] are not parallel since they are based on intrinsically sequential recurrences. However, we show that an appropriate reordering of the nodes and definition of the blocks in $\mathbf{A}$ lead to a large amount of parallelism. In practical cases for nano-transistor simulations, we found that the communication time was small and that the scalability was very good, even for small benchmark cases. This scheme is based on a combination of domain decomposition and cyclic reduction techniques ${ }^{1}$ (see, for example, Varga and Hageman [16]). Section 6 has some numerical results.

## 2. Recurrence formulas for the inverse matrix

Consider a general matrix A written in block form:

$$
\mathbf{A} \stackrel{\text { def }}{=}\left[\begin{array}{ll}
\mathbf{A}_{11} & \mathbf{A}_{12}  \tag{8}\\
\mathbf{A}_{21} & \mathbf{A}_{22}
\end{array}\right]
$$

From the LDU factorization of $\mathbf{A}$, we can form the factors $\mathbf{L}^{S} \stackrel{\text { def }}{=} \mathbf{A}_{21} \mathbf{A}_{11}^{-1}, \mathbf{U}^{S} \stackrel{\text { def }}{=} \mathbf{A}_{11}^{-1} \mathbf{A}_{12}$, and the Schur complement $\mathbf{S} \stackrel{\text { def }}{=} \mathbf{A}_{22}-\mathbf{A}_{21} \mathbf{A}_{11}^{-1} \mathbf{A}_{12}$. The following equation holds for the inverse matrix $\mathbf{G}=\mathbf{A}^{-1}$ :

$$
\mathbf{G}=\left[\begin{array}{cc}
\mathbf{A}_{11}^{-1}+\mathbf{U}^{\mathrm{S}} \mathbf{S}^{-1} \mathbf{L}^{\mathrm{s}} & -\mathbf{U}^{\mathrm{S}} \mathbf{S}^{-1}  \tag{9}\\
-\mathbf{S}^{-1} \mathbf{L}^{\mathrm{S}} & \mathbf{S}^{-1}
\end{array}\right]
$$

This equation can be verified by direct multiplication with $\mathbf{A}$. It allows computing the inverse matrix using backward recurrence formulas. These formulas can be obtained by considering step $i$ of the LDU factorization of $\mathbf{A}$, which has the following form:

$$
\mathbf{A}=\left[\begin{array}{cc}
\mathbf{L}(1: i-1,1: i-1) & \mathbf{0}  \tag{10}\\
\mathbf{L}(i: n, 1: i-1) & \mathbf{I}
\end{array}\right]\left[\begin{array}{cc}
\mathbf{D}(1: i-1,1: i-1) & \mathbf{0} \\
\mathbf{0} & \mathbf{S}^{i}
\end{array}\right]\left[\begin{array}{cc}
\mathbf{U}(1: i-1,1: i-1) & \mathbf{U}(1: i-1, i: n) \\
\mathbf{0} & \mathbf{I}
\end{array}\right]
$$

The Schur complement matrices $\boldsymbol{S}^{i}$ are defined by this equation for all $i$. From Eq. (10), the first step of the LDU factorization of $\mathbf{S}^{i}$ is the same as the $i+1$ th step in the factorization of $\mathbf{A}$ :

$$
\mathbf{S}^{i}=\left[\begin{array}{cc}
\mathbf{I} & \mathbf{0}  \tag{11}\\
\mathbf{L}(i+1: n, i) & \mathbf{I}
\end{array}\right]\left[\begin{array}{cc}
\mathbf{d}_{i i} & \mathbf{0} \\
\mathbf{0} & \mathbf{S}^{i+1}
\end{array}\right]\left[\begin{array}{cc}
\mathbf{I} & \mathbf{U}(i, i+1: n) \\
\mathbf{0} & \mathbf{I}
\end{array}\right]
$$

Combining Eqs. (9) and (11) with the substitutions:

$$
\begin{aligned}
& \mathbf{A}_{11} \rightarrow \mathbf{d}_{i i} \quad \mathbf{U}^{\mathrm{S}} \rightarrow \mathbf{U}(i, i+1: n) \\
& \mathbf{S} \rightarrow \mathbf{S}^{i+1} \quad \mathbf{L}^{\mathrm{S}} \rightarrow \mathbf{L}(i+1: n, i)
\end{aligned}
$$

we arrive at:

$$
\left[\mathbf{S}^{i}\right]^{-1}=\left[\begin{array}{cc}
\mathbf{d}_{i i}^{-1}+\mathbf{U}(i, i+1: n)\left[\mathbf{S}^{i+1}\right]^{-1} \mathbf{L}(i+1: n, i) & -\mathbf{U}(i, i+1: n)\left[\mathbf{S}^{i+1}\right]^{-1} \\
-\left[\mathbf{S}^{i+1}\right]^{-1} \mathbf{L}(i+1: n, i) & {\left[\mathbf{S}^{i+1}\right]^{-1}}
\end{array}\right]
$$

From Eq. (9), we have:

$$
\left[\mathbf{S}^{i}\right]^{-1}=\mathbf{G}(i: n, i: n), \text { and }\left[\mathbf{S}^{i+1}\right]^{-1}=\mathbf{G}(i+1: n, i+1: n)
$$

[^1]

Fig. 1. Schematics of how the recurrence formulas are used to calculate $\mathbf{G}$.

We have therefore proved the following backward recurrence relations:

$$
\begin{array}{|l|}
\hline \mathbf{G}(i+1: n, i)=-\mathbf{G}(i+1: n, i+1: n) \mathbf{L}(i+1: n, i) \\
\mathbf{G}(i, i+1: n)=-\mathbf{U}(i, i+1: n) \mathbf{G}(i+1: n, i+1: n)  \tag{12}\\
\mathbf{g}_{i i}=\mathbf{d}_{i i}^{-1}+\mathbf{U}(i, i+1: n) \mathbf{G}(i+1: n, i+1: n) \mathbf{L}(i+1: n, i)
\end{array}
$$

starting from $\mathbf{g}_{n n}=\mathbf{d}_{n n}^{-1}$. These equations are identical to Eqs. (5)-(7), respectively. A recurrence for $i=n-1$ down to $i=1$ can therefore be used to calculate G. See Fig. 1.

We have assumed that the matrices produced in the algorithms are all invertible (when required), and this may not be true in general. However, this has been the case in all practical applications encountered by the authors so far, for problems originating in electronic structure theory.

By themselves, these recurrence formulas do not lead to any reduction in the computational cost. However, we now show that even though the matrix $\mathbf{G}$ is full, we do not need to calculate all the entries. We denote $\mathbf{L}^{\text {sym }}$ and $\mathbf{U}^{\text {sym }}$ the lower and upper triangular factors in the symbolic factorization of $\mathbf{A}$. The non-zero structure of $\left(\mathbf{L}^{\text {sym }}\right)^{T}$ and $\left(\mathbf{U}^{\text {sym }}\right)^{T}$ is denoted by $Q$, that is $Q$ is the set of all pairs $(i, j)$ such that $\ell_{j i}^{\text {sym }} \neq \mathbf{0}$ or $\mathbf{u}_{j i}^{\text {sym }} \neq \mathbf{0}$. Then:

$$
\mathbf{g}_{i j}= \begin{cases}-\sum_{l>j,(i, l) \in Q} \mathbf{g}_{i l} \ell_{l j} & \text { if } i>j  \tag{13}\\ -\sum_{k>i,(k, j) \in Q} \mathbf{u}_{i k} \mathbf{g}_{k j} & \text { if } i<j \\ \mathbf{d}_{i i}^{-1}+\sum_{k>i, l>i,(k, l) \in Q} \mathbf{u}_{i k} \mathbf{g}_{k l} \ell_{l i} & \text { if } i=j\end{cases}
$$

where $(i, j) \in Q$.
Proof. Assume that $(i, j) \in Q, i>j$, then $\mathbf{u}_{j i}^{\text {sym }} \neq 0$. Assume also that $\ell_{l j}^{\text {sym }} \neq 0, l>j$, then by properties of the Gaussian elimination $(i, l) \in Q$. This proves the first case. The case $i<j$ can be proved similarly. For the case $i=j$, assume that $\mathbf{u}_{i k}^{\text {sym }} \neq 0$ and $\ell_{l i}^{\text {sym }} \neq 0$, then by properties of the Gaussian elimination $(k, l) \in Q$.

This implies that we do not need to calculate all the entries in $\mathbf{G}$ but rather only the entries in $\mathbf{G}$ corresponding to indices in $Q$. This represents a significant reduction in computational cost. The cost of computing the entries in $\mathbf{G}$ in the set $Q$ is the same (up to a constant factor) as the LDU factorization.

Similar results can be derived for the $\mathbf{G}^{<}$matrix:

$$
\begin{equation*}
\mathbf{G}^{<}=\mathbf{G} \boldsymbol{\Gamma} \mathbf{G}^{\dagger} \tag{14}
\end{equation*}
$$

if we assume that $\Gamma$ has the same sparsity pattern as $A$, that is: $\gamma_{i j} \neq 0 \Rightarrow \mathbf{a}_{i j} \neq 0$. The block $\gamma_{i j}$ denotes the block $(i, j)$ of matrix $\boldsymbol{\Gamma}$. To calculate the recurrences, we use the LDU factorization of $\mathbf{A}$ introduced previously. The matrix $\mathbf{G}^{<}$satisfies:

$$
\mathbf{A G}^{<} \mathbf{A}^{\dagger}=\boldsymbol{\Gamma}
$$

Using the same block notations as on page 5 , we multiply this equation to the left by $(\mathbf{L D})^{-1}$ and to the right by $(\mathbf{L D})^{-\dagger}$ :

$$
\left[\begin{array}{cc}
\mathbf{I} & \mathbf{U}^{\mathrm{S}} \\
\mathbf{0} & \mathbf{S}
\end{array}\right] \mathbf{G}^{<}\left[\begin{array}{cc}
\mathbf{I} & \mathbf{0} \\
\left(\mathbf{U}^{\mathrm{S}}\right)^{\dagger} & \mathbf{S}^{\dagger}
\end{array}\right]=\left[\begin{array}{cc}
\mathbf{A}_{11} & \mathbf{0} \\
\mathbf{L}^{\mathrm{S}} \mathbf{A}_{11} & \mathbf{I}
\end{array}\right]^{-1} \boldsymbol{\Gamma}\left[\begin{array}{cc}
\mathbf{A}_{11} & \mathbf{0} \\
\mathbf{L}^{\mathrm{S}} \mathbf{A}_{11} & \mathbf{I}
\end{array}\right]^{-\dagger}
$$

The equation above can be expanded as:

Using steps similar to the proof of Eqs. (12) and (15) leads to a forward recurrence that is performed in combination with the LDU factorization.

Let us define $\Gamma^{1} \stackrel{\text { def }}{=} \boldsymbol{\Gamma}$, then, for $1 \leqslant i \leqslant n-1$ :

$$
\begin{align*}
& \Gamma_{\mathrm{L}}^{i+1} \stackrel{\text { def }}{=}\left(\boldsymbol{\Gamma}_{21}^{i}-\mathbf{L}(i+1: n, i) \boldsymbol{\gamma}_{11}^{i}\right) \mathbf{d}_{1 i}^{-\dagger}  \tag{16}\\
& \boldsymbol{\Gamma}_{\mathrm{U}}^{i+1} \stackrel{\text { def }}{=} \mathbf{d}_{i i}^{-1}\left(\boldsymbol{\Gamma}_{12}^{i}-\gamma_{11}^{i} \mathbf{L}(i+1: n, i)^{\dagger}\right)  \tag{17}\\
& \Gamma^{i+1} \stackrel{\text { def }}{=} \boldsymbol{\Gamma}_{22}^{i}-\mathbf{L}(i+1: n, i) \boldsymbol{\Gamma}_{12}^{i}-\boldsymbol{\Gamma}_{21}^{i} \mathbf{L}(i+1: n, i)^{\dagger}+\mathbf{L}(i+1: n, i) \gamma_{11}^{i} \mathbf{L}(i+1: n, i)^{\dagger} \tag{18}
\end{align*}
$$

with

$$
\left[\begin{array}{ll}
\boldsymbol{\gamma}_{11}^{i} & \boldsymbol{\Gamma}_{12}^{i} \\
\boldsymbol{\Gamma}_{21}^{i} & \boldsymbol{\Gamma}_{22}^{i}
\end{array}\right] \stackrel{\text { def }}{=} \boldsymbol{\Gamma}^{i}
$$

and the following block sizes (in terms of number of blocks of the sub-matrix):

| Sub-matrix | Size (blocks) |
| :--- | :--- |
| $\gamma_{11}^{i}$ | $1 \times 1$ |
| $\Gamma_{12}^{i}$ | $1 \times(n-i+1)$ |
| $\Gamma_{21}^{i}$ | $(n-i+1) \times 1$ |
| $\Gamma_{22}^{i}$ | $(n-i+1) \times(n-i+1)$ |

Note that the $\mathbf{U}$ factors are not needed in this process. Once this recurrence is complete, we can perform a backward recurrence to find $\mathbf{G}^{<}$, as in Eq. (12). This recurrence can be proved using Eq. (15):

$$
\begin{array}{|l|}
\hline \mathbf{G}^{<}(i+1: n, i)=\mathbf{G}(i+1: n, i+1: n) \Gamma_{\mathrm{L}}^{i+1}-\mathbf{G}^{<}(i+1: n, i+1: n) \mathbf{U}(i, i+1: n)^{\dagger} \\
\mathbf{G}^{<}(i, i+1: n)=\Gamma_{\mathrm{U}}^{i+1} \mathbf{G}(i+1: n, i+1: n)^{\dagger}-\mathbf{U}(i, i+1: n) \mathbf{G}^{<}(i+1: n, i+1: n) \\
\mathbf{g}_{i i}^{<}=\mathrm{d}_{i i}^{-1} \gamma_{11}^{i} \mathrm{~d}_{i i}^{-\dagger}-\mathbf{U}(i, i+1: n) \mathbf{G}^{<}(i+1: n, i)-\mathbf{G}^{<}(i, i+1: n) \mathbf{U}(i, i+1: n)^{\dagger}  \tag{19}\\
\quad-\mathbf{U}(i, i+1: n) \mathbf{G}^{<}(i+1: n, i+1: n) \mathbf{U}(i, i+1: n)^{\dagger}
\end{array}
$$

starting from $\mathbf{g}_{n n}^{\llcorner }=\mathbf{g}_{n n} \Gamma^{n} \mathbf{g}_{n n}^{\dagger}$. The proof is omitted but is similar to the one for Eq. (12).
As before, the computational cost can be reduced significantly by taking advantage of the fact that the calculation can be restricted to indices $(i, j)$ in $Q$. For this, we need to further assume that $(i, j) \in Q \Longleftrightarrow(j, i) \in Q$.

First, we observe that the cost of computing $\Gamma^{i}, \Gamma_{\mathrm{L}}^{i}, \Gamma_{\mathrm{U}}^{i}$ for all $i$ is of the same order as the LDU factorization (i.e., equal up to a constant multiplicative factor). This can be proved by observing that calculating $\Gamma^{i+1}$ using Eq. (18) leads to the same fill-in as the Schur complement calculations for $\mathbf{S}^{i+1}$.

Second, the set of Eq. (19) for $\mathbf{G}^{<}$can be simplified to:
where $(i, j) \in Q$. (Notation clarification: $\gamma_{j l}^{i},\left[\gamma_{\mathrm{L}}^{i}\right]_{j l}$, and $\left[\gamma_{\mathrm{U}}^{i}\right]_{j l}$ are respectively blocks of $\Gamma^{i}, \Gamma_{\mathrm{L}}^{i}$, and $\Gamma_{\mathrm{U}}^{i}$.) The proof of this result is similar to the one for Eq. (13) for $\mathbf{G}$. The reduction in computational cost is realized because all the sums are restricted to indices in $Q$.

Observe that the calculation of $\mathbf{G}$ and $\Gamma^{i}$ depends only on the LDU factorization, while the calculation of $\mathbf{G}^{<}$depends also on $\Gamma^{i}$ and $\mathbf{G}$.

## 3. Sequential algorithm for 1D problems

In this section, we present a sequential implementation of the relations presented above, for 1D problems. Section 5 will discuss the parallel implementation.

In 1 dimensional problems, matrix $\mathbf{A}$ assumes a block tridiagonal form. The LDU factorization is obtained using the following recurrences:

$$
\begin{align*}
& \ell_{i+1, i}=\mathbf{a}_{i+1, i} \mathbf{d}_{i i}^{-1}  \tag{21}\\
& \mathbf{u}_{i, i+1}=\mathbf{d}_{i i}^{-1} \mathbf{a}_{i, i+1}  \tag{22}\\
& \mathbf{d}_{i+1, i+1}=\mathbf{a}_{i+1, i+1}-\mathbf{a}_{i+1, i} \mathbf{d}_{i i}^{-1} \mathbf{a}_{i, i+1}=\mathbf{a}_{i+1, i+1}-\ell_{i+1, i} \mathbf{a}_{i, i+1}=\mathbf{a}_{i+1, i+1}-\mathbf{a}_{i+1, i} \mathbf{u}_{i, i+1} \tag{23}
\end{align*}
$$

From Eq. (13), the backward recurrences for $\mathbf{G}$ are given by:

$$
\begin{align*}
& \mathbf{g}_{i+1, i}=-\mathbf{g}_{i+1, i+1} \ell_{i+1, i}  \tag{24}\\
& \mathbf{g}_{i, i+1}=-\mathbf{u}_{i, i+1} \mathbf{g}_{i+1, i+1}  \tag{25}\\
& \mathbf{g}_{i i}=\mathbf{d}_{i i}^{-1}+\mathbf{u}_{i, i+1} \mathbf{g}_{i+1, i+1} \ell_{i+1, i}=\mathbf{d}_{i i}^{-1}-\mathbf{g}_{i, i+1} \ell_{i+1, i}=\mathbf{d}_{i i}^{-1}-\mathbf{u}_{i, i+1} \mathbf{g}_{i+1, i} \tag{26}
\end{align*}
$$

starting with $\mathbf{g}_{n n}=\mathbf{d}_{n n}^{-1}$. To calculate $\mathbf{G}^{<}$, we first need to compute $\Gamma^{i}$. Considering only the non-zero entries in the forward recurrence Eqs. (16)-(18), we need to calculate:

$$
\begin{aligned}
& \gamma_{\mathrm{L}}^{i+1} \stackrel{\text { def }}{=}\left(\gamma_{i+1, i}-\ell_{i+1, i} \gamma^{i}\right) \mathbf{d}_{i i}^{-\dagger} \\
& \gamma_{\mathrm{U}}^{i+1} \stackrel{\text { def }}{=} \mathbf{d}_{i i}^{-1}\left(\gamma_{i, i+1}-\gamma^{i} \ell_{i+1, i}^{\dagger}\right) \\
& \gamma^{i+1} \stackrel{\text { def }}{=} \gamma_{i+1, i+1}-\ell_{i+1, i} \gamma_{i, i+1}-\gamma_{i+1, i} \ell_{i+1, i}^{\dagger}+\ell_{i+1, i} \gamma^{i} \ell_{i+1, i}^{\dagger}
\end{aligned}
$$

starting from $\gamma^{1} \stackrel{\text { def }}{=} \gamma_{11}$ (the (1,1) block in matrix $\Gamma$ ); $\boldsymbol{\gamma}^{i}, \gamma_{L}^{i}$, and $\gamma_{U}^{i}$ are $1 \times 1$ blocks. For simplicity, we have shortened the notations. The blocks $\gamma^{i}, \gamma_{\mathrm{L}}^{i}$, and $\gamma_{\mathrm{U}}^{i}$ are in fact the $(1,1)$ block of $\Gamma^{i}, \Gamma_{\mathrm{L}}^{i}$, and $\Gamma_{\mathrm{U}}^{i}$.

Once the factors $\mathbf{L}, \mathbf{U}, \mathbf{G}, \gamma^{i}, \gamma_{\mathrm{L}}^{i}$, and $\gamma_{U}^{i}$ have been computed, the backward recurrence Eq. (19) for $\mathbf{G}^{<}$can be computed:

$$
\begin{aligned}
& \mathbf{g}_{i+1, i}^{<}=\mathbf{g}_{i+1, i+1} \gamma_{\mathrm{L}}^{i+1}-\mathbf{g}_{i+1, i+1}^{<} \mathbf{u}_{i, i+1}^{\dagger} \\
& \mathbf{g}_{i, i+1}^{<}=\gamma_{\mathrm{U}}^{i+1} \mathbf{g}_{i+1, i+1}^{\dagger}-\mathbf{u}_{i, i+1} \mathbf{g}_{i+1, i+1}^{\llcorner } \\
& \mathbf{g}_{i i}^{<}=\mathbf{d}_{i i}^{-1} \gamma^{i} \mathbf{d}_{i i}^{-\dagger}-\mathbf{u}_{i, i+1} \mathbf{g}_{i+1, i}^{<}-\mathbf{g}_{i, i+1}^{<} \mathbf{u}_{i, i+1}^{\dagger}-\mathbf{u}_{i, i+1} \mathbf{g}_{i+1, i+1}^{<} \mathbf{u}_{i, i+1}^{\dagger}
\end{aligned}
$$

starting from $\mathbf{g}_{n n}^{<}=\mathbf{g}_{n n} \gamma^{n} \mathbf{g}_{n}^{\dagger}$.
These recurrences are the most efficient way to calculate $\mathbf{G}$ and $\mathbf{G}^{<}$on a sequential computer. On a parallel computer, this approach is of limited use since there is little room for parallelization. In Section 5 we describe a scalable algorithm to perform the same calculations in a truly parallel fashion.

## 4. Computational cost for 1D and 2D cartesian meshes with nearest neighbor stencils

In this section, we determine the computational cost of the sequential algorithms. From the recurrence relations (13) and (20), one can prove that the computational cost of calculating $\mathbf{G}$ and $\mathbf{G}^{<}$has the same scaling with problem size as the LDU factorization. If one uses a nested dissection ordering associated with the mesh [17], we obtain the following costs for Cartesian grids assuming a local discretization stencil:

| Dimension | Cost |
| :--- | :--- |
| 1D | $O\left(n d^{3}\right)$ |
| 2D (square grid) | $O\left(n^{3 / 2} d^{3}\right)$ |
| 3D (cubic grid) | $O\left(n^{2} d^{3}\right)$ |

We now do a more detailed analysis of the computational cost and a comparison with the FIND algorithm of Li et al. [14]. This algorithm comprises an LDU factorization and a backward recurrence.

For the 1D case, the LDU factorization needs $4 d^{3}$ flops for each column: $d^{3}$ for computing $\mathbf{d}_{i i}^{-1}, d^{3}$ for $\ell_{i+1, i}, d^{3}$ for $\mathbf{u}_{i, i+1}$, and $d^{3}$ for $\mathbf{d}_{i+1, i+1}$. So the total computational cost of LDU factorization is $4 n d^{3}$ flops. Following Eq. (13), the cost of the backward recurrence is $3 n d^{3}$. Note that the explicit form of $\mathbf{d}_{i i}^{-1}$ is not needed in the LDU factorization. However, the explicit form of $\mathbf{d}_{i i}^{-1}$ is needed for the backward recurrence and computing it in the LDU factorization reduces the total cost. In the above analysis, we have therefore included the cost $d^{3}$ of obtaining $\mathbf{d}_{i i}^{-1}$ during the LDU factorization. The total cost in 1D is therefore:

$$
7 n d^{3}
$$

For the 2D case, similar to the nested dissection method [17] and FIND algorithm [14], we decompose the mesh in a hierarchical way. See Fig. 2. First we split the mesh in two parts (see the upper and lower parts of the mesh on the right panel of Fig. 2). This is done by identifying a set of nodes called the separator. They are numbered 31 in the right panel. The separator is such that it splits the mesh into 3 sets: set $s_{1}, s_{2}$ and the separator $s$ itself. It satisfies the following properties: (i)


Fig. 2. The cluster tree (left) and the mesh nodes (right) for mesh of size $15 \times 15$. The number in each tree node indicates the cluster number. The number in each mesh node indicates the cluster it belongs to.
$\mathbf{a}_{i j}=0$ if $i$ belongs to $s_{1}$ and $j$ to $s_{2}$, and vice versa (this is a separator set); (ii) for every $i$ in $s$, there is at least one index $j_{1}$ in $s_{1}$ and one index $j_{2}$ in $s_{2}$ such that $\mathbf{a}_{i j_{1}} \neq 0$ and $\mathbf{a}_{i_{j}} \neq 0$ (the set is minimal). Once the mesh is split into $s_{1}$ and $s_{2}$ the process is repeated recursively, thereby building a tree decomposition of the mesh. In Fig. 2, clusters 17-31 are all separators [17]. When we perform the LDU factorization, we eliminate the nodes corresponding to the lower level clusters first and then those in the higher level clusters.

Compared to nested dissection [17], we use vertical and horizontal separators instead of cross shaped separators to make the estimates of computational cost easier to derive. Compared to FIND [14], we use single separators | here instead of double separators $\|$ since we do not need additional "independence" among clusters as required in [14] for multiple LU factorizations. See [14] for additional details regarding this distinction.

To estimate the computational cost, we need to consider the boundary nodes of a given cluster. Boundary nodes are nodes of a cluster that are connected to nodes outside the cluster. Since the non-zero pattern of the matrix changes as the elimination is proceeding, we need to consider the evolving connectivity of the mesh. For example, the nodes labeled 22 become connected to nodes 27 and 30 after nodes 11 and 12 have been eliminated; similarly, the nodes 20 become connected to nodes 26, 31 and 29. This is shown in Fig. 2. For more details, see [17,14].

Once the separators and boundaries are determined, we see that the computational cost of eliminating a separator is equal to $s b^{2}+2 s^{2} b+\frac{1}{3} s^{3}=s(b+s)^{2}-\frac{2}{3} s^{3}$ flops, where $s$ is the number of nodes in the separator set and $b$ is the number of nodes in the boundary set. As in the 1D case, we include the cost for $\mathbf{d}_{i i}^{-1}$.

To make it simple, we focus on a 2D square mesh with $N$ nodes and the typical nearest neighbor connectivity. If we ignore the effect of the boundary of the mesh, the size of the separators within a given level is fixed. If the ratio $b / s$ is constant, then the cost for each separator is proportional to $s^{3}$ and the number of separators is proportional to $N / s^{2}$, so the cost for each level doubles every two levels. The computational costs thus form a geometric series and the top level cost dominates the total cost.

When we take the effect of the mesh boundary in consideration, the value of $b$ for a cluster near the boundary of the mesh needs to be adjusted. For lower levels, such clusters form only a small fraction of all the clusters and thus the effect is not significant. For top levels, however, such effect cannot be ignored. Since the cost for the top levels dominates the total cost, we need to calculate the computational cost for top levels more precisely.

Table 2 shows the cost for the top level clusters. The last two rows are the upper bound of the total cost for the rest of the small clusters.

If we compute the cost for each level and sum them together, we obtain a cost of $24.9 N^{3 / 2}$ flops for the LDU factorization.
For the backward recurrence, we have the same sets of separators. Each node in the separator is connected to all the other nodes in the separator and all the nodes in the boundary set. Since we have an upper triangular matrix now, when we deal with a separator of size $s$ with $b$ nodes on the boundary, the number of non-zero entries in each row increases from $b$ to $s+b$. As a result, the cost for computing Eq. (13) is $3\left(b^{2} s+b s^{2}+\frac{1}{3} s^{3}\right)$ flops for each step. The total computational cost for the backward recurrence is then $61.3 N^{3 / 2}$ flops. The costs for each type of clusters are also listed in Table 2.

Adding together the cost of the LDU factorization and the backward recurrence, the total computational cost for the algorithm is $86.2 N^{3 / 2}$ flops. For FIND, the cost is $147 N^{3 / 2}$ flops [18]. Note that these costs are for the serial version of the two algorithms. Although the cost is reduced roughly by half compared to FIND, the parallelization of FIND is different and therefore the running time of both algorithms on parallel platforms may scale differently.

Table 2
Estimate of the computational cost for a 2D square mesh for different cluster sizes. The size is in unit of $N^{1 / 2}$. The cost is in unit of $N^{3 / 2}$ flops.

| Size of cluster |  | Cost per cluster |  | Level | Number of clusters |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Separator | Boundary | LDU | Back. Recurr. |  |  |
| 1/2 | 1 | 1.042 | 2.375 | 1 | 2 |
| 1/2 | 1 | 1.042 | 2.375 | 2 | 4 |
| 1/4 | 5/4 | 0.552 | 1.422 | 3 | 4 |
| 1/4 | 3/4 | 0.240 | 0.578 | 3 | 4 |
| 1/4 | 1 | 0.380 | 0.953 | 4 | 4 |
| 1/4 | 3/4 | 0.240 | 0.578 | 4 | 8 |
| 1/4 | 1/2 | 0.130 | 0.297 | 4 | 4 |
| 1/8 | 3/4 | 0.094 | 0.248 | 5 | 8 |
| 1/8 | 5/8 | 0.069 | 0.178 | 5 | 24 |
| 1/8 | 1/2 | 0.048 | 0.119 | 6 | 64 |
| 1/16 | 3/8 | 0.012 | 0.031 | 7 | 128 |
| 1/8 | 1/8 | 0.048 | 0.119 | $8 \ldots$ | $\leqslant 64$ |
| 1/16 | 3/8 | 0.012 | 0.031 | $9 \ldots$ | $\leqslant 128$ |

We now focus on the implementation of these recurrence relations for the calculation of $\mathbf{G}$ in the 1D case on a parallel computer. Similar ideas can be applied to the calculation of $\mathbf{G}^{<}$. The parallel implementation of the 2D case is significantly more complicated and will be the subject of another paper.

## 5. Parallel algorithm for 1D problems

We present a parallel algorithm for the calculation of the Green's function matrix $\mathbf{G}$ typically encountered in electronic structure calculations where the matrix $\mathbf{A}$ (cf. Eq. (1)) is assumed to be an $n \times n$ block matrix, and in block tridiagonal form as shown by

$$
\mathbf{A}=\left[\begin{array}{ccccc}
\mathbf{a}_{11} & \mathbf{a}_{12} & & &  \tag{27}\\
\mathbf{a}_{21} & \mathbf{a}_{22} & \mathbf{a}_{23} & & \\
& \mathbf{a}_{32} & \mathbf{a}_{33} & \mathbf{a}_{34} & \\
& & \ddots & \ddots & \ddots
\end{array}\right]
$$

where each block element $\mathbf{a}_{i j}$ is a dense complex matrix. In order to develop a parallel algorithm, we assume that we have at our disposal a total of $\mathcal{P}$ processing elements (e.g., single core on a modern processor). We also consider that we have processes with the convention that each process is associated with a unique processing element, and we assume that they can communicate among themselves. The processes are labeled $p_{0}, p_{1}, \ldots, p_{\mathcal{P}-1}$. The block tridiagonal structure $\mathbf{A}$ is then distributed among these processes in a row-striped manner, as illustrated in Fig. 3.

Thus each process is assigned ownership of certain contiguous rows of $\mathbf{A}$. This ownership arrangement, however, also extends to the calculated blocks of the inverse $\mathbf{G}$, as well as any LU factors determined during calculation in $\mathbf{L}$ and $\mathbf{U}$. The manner of distribution is an issue of load balancing, and will be addressed later in the paper.

Furthermore, for illustrative purposes, we present the block matrix structures as having identical block sizes throughout. The algorithm presented has no condition for the uniformity of block sizes in $\mathbf{A}$, and can be applied to a block tridiagonal matrix as presented on the right in Fig. 3. The size of the blocks throughout A, however, does have consequences for adequate load balancing.


Fig. 3. Two different block tridiagonal matrices distributed among 4 different processes, labeled $p_{0}, p_{1}, p_{2}$ and $p_{3}$.


Fig. 4. The distinct phases of operations performed by the hybrid method in determining $\operatorname{Trid}_{\mathbf{A}}\{\mathbf{G}\}$, the block tridiagonal portion of $\mathbf{G}$ with respect to the structure of $\mathbf{A}$. The block matrices in this example are partitioned across 4 processes, as indicated by the horizontal dashed lines.

For the purposes of electronic structure applications, we only require the portion of the inverse $\mathbf{G}$ with the same block tridiagonal structure of $\mathbf{A}$. We express this portion as $\operatorname{Trid}_{\mathbf{A}}\{\mathbf{G}\}$.

The parallel algorithm is a hybrid technique in the sense that it combines the techniques of cyclic reduction and Schur block decomposition, but where we now consider individual elements to be dense matrix blocks. The steps taken by the hybrid algorithm to produce $\operatorname{Trid}_{\mathbf{A}}\{\mathbf{G}\}$ is outlined in Fig. 4.

The algorithm begins with our block tridiagonal matrix A partitioned across a number of processes, as indicated by the dashed horizontal lines. Each process then performs what is equivalent to calculating a Schur complement on the rows/ blocks that it owns, leaving us with a reduced system that is equivalent to a smaller block tridiagonal matrix $\mathbf{A}^{\text {schur }}$. This phase, which we name the Schur reduction phase, is entirely devoid of interprocess communication.

It is on this smaller block tridiagonal structure that we perform block cyclic reduction (BCR), leaving us with a single block of the inverse, $\mathbf{g}_{k k}$. This block cyclic reduction phase involves interprocess communication.

From $\mathbf{g}_{k k}$ we then produce the portion of the inverse corresponding to $\mathbf{G}^{B C R}=\operatorname{Trid}_{A^{\text {schur }}}\{\mathbf{G}\}$ in what we call the block cyclic production phase. This is done using Eq. (12). Finally, using $\mathbf{G}^{B C R}$, we can then determine the full tridiagonal structure of $\mathbf{G}$ that we desire without any further need for interprocess communication through a so-called Schur production phase. The block cyclic production phase and Schur production phase are a parallel implementation of the backward recurrences in Eq. (12).

### 5.1. Schur phases

In order to illustrate where the equations for the Schur reduction and production phases come from, we perform them for small examples in this section. Furthermore, this will also serve to illustrate how our hybrid method is equivalent to unpivoted block Gaussian elimination.

### 5.1.1. Corner block operations

Looking at the case of Schur corner reduction, we take the following block form as our starting point:

$$
\mathbf{A}=\left[\begin{array}{ccc}
\mathbf{a}_{i i} & \mathbf{a}_{i j} & \mathbf{0} \\
\mathbf{a}_{j i} & \mathbf{a}_{j j} & \star \\
\mathbf{0} & \star & \star
\end{array}\right]
$$

where $\star$ denotes some arbitrary entries (zero or not). In eliminating the block $\mathbf{a}_{i i}$, we calculate the following LU factors

$$
\begin{aligned}
\ell_{j i} & =\mathbf{a}_{j i} \mathbf{a}_{i i}^{-1} \\
\mathbf{u}_{i j} & =\mathbf{a}_{i i}^{-1} \mathbf{a}_{i j}
\end{aligned}
$$

and we determine the Schur block

$$
\mathbf{s} \stackrel{\text { def }}{=} \mathbf{a}_{j j}-\ell_{j i} \mathbf{a}_{i j} .
$$



Fig. 5. Three different schemes that represent a corner production step undertaken in the BCR production phase, where we produce inverse blocks on row/ column $i$ using inverse blocks and LU factors from row/column $j$.

Let us now assume that the inverse block $\mathbf{g}_{\mathrm{j} j}$ has been calculated. We then start the Schur corner production phase in which, using the LU factors saved from the reduction phase, we can obtain

$$
\begin{aligned}
\mathbf{g}_{j i} & =-\mathbf{g}_{j j} \ell_{j i} \\
\mathbf{g}_{i j} & =-\mathbf{u}_{i j} \mathbf{g}_{j j}
\end{aligned}
$$

(see Eq. (12)) and finally

$$
\begin{align*}
\mathbf{g}_{i i} & =\mathbf{a}_{i i}^{-1}+\mathbf{u}_{i j} \mathbf{g}_{i j} \ell_{j i}  \tag{28}\\
& =\mathbf{a}_{i i}^{-1}-\mathbf{u}_{i j} \mathbf{g}_{j i}  \tag{29}\\
& =\mathbf{a}_{i i}^{-1}-\mathbf{g}_{i j} \ell_{j i} . \tag{30}
\end{align*}
$$

This production step is visualized in Fig. 5. On the top right of the figure we have the stored LU factors preserved from the BCR elimination phase. On the top left, bottom right and bottom left we see three different schemes for producing the new inverse blocks $\mathbf{g}_{i i}, \mathbf{g}_{i j}$ and $\mathbf{g}_{j i}$ from $\mathbf{g}_{j j}$ and the LU factors $\mathbf{u}_{i j}$ and $\ell_{j i}$, corresponding to Eqs. (28)-(30). A solid arrow indicates the need for the corresponding block of the inverse matrix and a dashed arrow indicates the need for the corresponding LU block.

Using this figure, we can determine what matrix blocks are necessary to calculate one of the desired inverse blocks. Looking at a given inverse block, the required information is indicated by the inbound arrows. The only exception is $\mathbf{g}_{i i}$ which also requires the block $\mathbf{a}_{i i}$.

Three different schemes to calculate $\mathbf{g}_{i i}$ are possible, since the block can be determined via any of the three equations Eqs. (28) and (29) or Eq. (30), corresponding respectively to the upper left, lower right and lower left schemes in Fig. 5.

Assuming we have process $p_{i}$ owning row $i$ and process $p_{j}$ owning row $j$, we disregard choosing the lower left scheme (cf. Eq. (30)) since the computation of $\mathbf{g}_{i i}$ on process $p_{i}$ will have to wait for process $p_{j}$ to calculate and send $\mathbf{g}_{j i}$. This leaves us with the choice of either the upper left scheme (cf. Eq. (28)) or bottom right scheme (cf. Eq. (29)) where $\mathbf{g}_{j j}$ and $\ell_{j i}$ can be sent immediately, and both processes $p_{i}$ and $p_{j}$ can then proceed to calculate in parallel.

However, the bottom right scheme (cf. Eq. (29)) is preferable to the top left scheme (cf. Eq. (28)) since it saves an extra matrix-matrix multiplication. This motivates our choice of using Eq. (29) for our hybrid method.

### 5.1.2. Center block operations

The reduction/production operations undertaken by a center process begins with the following block tridiagonal form:

$$
\mathbf{A}=\left[\begin{array}{ccccc}
\star & \star & \mathbf{0} & \mathbf{0} & \mathbf{0} \\
\star & \mathbf{a}_{i i} & \mathbf{a}_{i j} & \mathbf{0} & \mathbf{0} \\
\mathbf{0} & \mathbf{a}_{j i} & \mathbf{a}_{j j} & \mathbf{a}_{j k} & \mathbf{0} \\
\mathbf{0} & \mathbf{0} & \mathbf{a}_{k j} & \mathbf{a}_{k k} & \star \\
\mathbf{0} & \mathbf{0} & \mathbf{0} & \star & \star
\end{array}\right]
$$

Through a block permutation matrix $\mathbf{P}$, we can transform it to the form

$$
\mathbf{P A P}=\left(\begin{array}{c|cccc}
\mathbf{a}_{j j} & \mathbf{a}_{j i} & \mathbf{a}_{j k} & 0 & 0 \\
\hline \mathbf{a}_{i j} & \mathbf{a}_{i i} & \mathbf{0} & \star & \mathbf{0} \\
\mathbf{a}_{k j} & 0 & \mathbf{a}_{k k} & 0 & \star \\
0 & \star & 0 & \star & 0 \\
0 & 0 & \star & 0 & \star
\end{array}\right)
$$

which we interpret as a $2 \times 2$ block matrix as indicated by the partitioning lines in the expression. We then perform a Schur complement calculation as done earlier for a corner process, obtaining parts of the LU factors

$$
\begin{equation*}
\text { Part of the L factor(column } j): \quad\binom{\mathbf{a}_{i j}}{\mathbf{a}_{k j}} \mathbf{a}_{j j}^{-1}=\binom{\ell_{i j}}{\ell_{k j}} \tag{31}
\end{equation*}
$$

$$
\begin{equation*}
\text { Part of the } U \text { factor(row } j): \quad \mathbf{a}_{i j}^{-1}\left(\mathbf{a}_{j i} \mathbf{a}_{j k}\right)=\left(\mathbf{u}_{j i} \mathbf{u}_{j k}\right) \tag{32}
\end{equation*}
$$

This then leads us to the $2 \times 2$ block Schur matrix

$$
\left(\begin{array}{cc}
\mathbf{a}_{i i} & \mathbf{0}  \tag{33}\\
\mathbf{0} & \mathbf{a}_{k k}
\end{array}\right)-\binom{\ell_{i j}}{\ell_{k j}}\left(\mathbf{a}_{j i} \mathbf{a}_{j k}\right)=\left(\begin{array}{cc}
\mathbf{a}_{i i}-\ell_{i j} \mathbf{a}_{j i} & -\ell_{i j} \mathbf{a}_{j k} \\
-\ell_{k j} \mathbf{a}_{j i} & \mathbf{a}_{k k}-\ell_{k j} \mathbf{a}_{j k}
\end{array}\right)
$$

Let us assume that we have now computed the following blocks of the inverse $\mathbf{G}$ :

$$
\left(\begin{array}{ll}
\mathbf{g}_{i i} & \mathbf{g}_{i k} \\
\mathbf{g}_{k i} & \mathbf{g}_{k k}
\end{array}\right)
$$

With this information, we can use the stored LU factors to determine the other blocks of the inverse [see Eq. (12)], getting

$$
\left(\mathbf{g}_{j i} \mathbf{g}_{j k}\right)=-\left(\mathbf{u}_{j i} \mathbf{u}_{j k}\right)\left(\begin{array}{ll}
\mathbf{g}_{i i} & \mathbf{g}_{i k} \\
\mathbf{g}_{k i} & \mathbf{g}_{k k}
\end{array}\right)=\left(-\mathbf{u}_{j i} \mathbf{g}_{i i}-\mathbf{u}_{j k} \mathbf{g}_{k i} \quad-\mathbf{u}_{j i} \mathbf{g}_{i k}-\mathbf{u}_{j k} \mathbf{g}_{k k}\right)
$$



Fig. 6. The three different schemes that represent a center production step undertaken in the BCR production phase, where we produce inverse block elements on row/column $j$ using inverse blocks and LU factors from rows $i$ and $k$.
and

$$
\binom{\mathbf{g}_{i j}}{\mathbf{g}_{k j}}=-\left(\begin{array}{ll}
\mathbf{g}_{i i} & \mathbf{g}_{i k} \\
\mathbf{g}_{k i} & \mathbf{g}_{k k}
\end{array}\right)\binom{\ell_{i j}}{\ell_{k j}}=\binom{-\mathbf{g}_{i i} \ell_{i j}-\mathbf{g}_{i k} \ell_{k j}}{-\mathbf{g}_{k i} \ell_{i j}-\mathbf{g}_{k k} \ell_{k j}} .
$$

The final block of the inverse is obtained as

$$
\mathbf{g}_{j j}=\mathbf{a}_{i j}^{-1}+\left(\mathbf{u}_{j i} \mathbf{u}_{j k}\right)\left(\begin{array}{cc}
\mathbf{g}_{i i} & \mathbf{g}_{i k}  \tag{34}\\
\mathbf{g}_{k i} & \mathbf{g}_{k k}
\end{array}\right)\binom{\ell_{i j}}{\ell_{k j}}=\mathbf{a}_{i j}^{-1}-\left(\mathbf{g}_{j i} \mathbf{g}_{j k}\right)\binom{\ell_{i j}}{\ell_{k j}}=\mathbf{a}_{j j}^{-1}-\mathbf{g}_{j i} \ell_{i j}-\mathbf{g}_{j k} \ell_{k j} .
$$

The Schur production step for a center block is visualized in Fig. 6, where the arrows are given the same significance as for a corner production step shown in Fig. 5.

Again, three different schemes arise since $\mathbf{g}_{j j}$ can be determined via one of the following three equations, depending on how $\mathbf{g}_{j j}$ from Eq. (34) is calculated:

$$
\begin{align*}
& \mathbf{g}_{j j}=\mathbf{a}_{j j}^{-1}+\mathbf{u}_{j i} \mathbf{g}_{i i} \ell_{i j}+\mathbf{u}_{j k} \mathbf{g}_{k i} \ell_{i j}+\mathbf{u}_{j i} \mathbf{g}_{i k} \ell_{k j}+\mathbf{u}_{j k} \mathbf{g}_{k k} \ell_{k j}  \tag{35}\\
& \mathbf{g}_{j j}=\mathbf{a}_{j j}^{-1}-\mathbf{u}_{j i} \mathbf{g}_{i j}-\mathbf{u}_{j k} \mathbf{g}_{k j}  \tag{36}\\
& \mathbf{g}_{j j}=\mathbf{a}_{j j}^{-1}-\mathbf{g}_{j i} \ell_{i j}-\mathbf{g}_{j k} \ell_{k j} \tag{37}
\end{align*}
$$

where Eqs. (35)-(37) corresponds to the upper left, lower right and lower left schemes in Fig. 6. Similarly motivated as for the corner Schur production step, we choose the scheme related to Eq. (37) corresponding to the lower left corner of Fig. 6.

### 5.2. Block cyclic reduction phases

Cyclic reduction operates on a regular tridiagonal linear system by eliminating the odd-numbered unknowns recursively, until only a single unknown remains, uncoupled from the rest of the system. One can then solve for this unknown, and from there descend down the recursion tree and obtain the full solution. In the case of block cyclic reduction, the individual scalar unknowns correspond to block matrices, but the procedure operates in the same manner. For our application, the equations are somewhat different. We have to follow Eq. (12) but otherwise the pattern of computation is similar. The basic operation in the reduction phase of $B C R$ is the row-reduce operation, where two odd-indexed rows are eliminated by row operations towards its neighbor. Starting with the original block tridiagonal form,

$$
\left(\begin{array}{ccccccc}
\star & \star & \star & \mathbf{0} & \mathbf{0} & \mathbf{0} & \mathbf{0} \\
\mathbf{0} & \mathbf{a}_{i h} & \mathbf{a}_{i i} & \mathbf{a}_{i j} & \mathbf{0} & \mathbf{0} & \mathbf{0} \\
\mathbf{0} & \mathbf{0} & \mathbf{a}_{j i} & \mathbf{a}_{j j} & \mathbf{a}_{j k} & \mathbf{0} & \mathbf{0} \\
\mathbf{0} & \mathbf{0} & \mathbf{0} & \mathbf{a}_{k j} & \mathbf{a}_{k k} & \mathbf{a}_{k l} & \mathbf{0} \\
\mathbf{0} & \mathbf{0} & \mathbf{0} & \mathbf{0} & \star & \star & \star
\end{array}\right),
$$

we reduce from the odd rows $i$ and $k$ towards the even row $j$, eliminating the coupling element $\mathbf{a}_{j i}$ by a row operation involving row $i$ and the factor $\ell_{j i}=\mathbf{a}_{j i} \mathbf{a}_{i i}^{-1}$. Likewise, we eliminate $\mathbf{a}_{j k}$ by a row operation involving row $k$ and the factor $\ell_{j k}=\mathbf{a}_{j k} \mathbf{a}_{k k}^{-1}$, and obtain

$$
\left(\begin{array}{ccccccc}
\star & \star & \star & \mathbf{0} & \mathbf{0} & \mathbf{0} & \mathbf{0}  \tag{38}\\
\mathbf{0} & \mathbf{a}_{i h} & \mathbf{a}_{i i} & \mathbf{a}_{i j} & \mathbf{0} & \mathbf{0} & \mathbf{0} \\
\mathbf{0} & \mathbf{a}_{j h}^{\mathrm{BCR}} & \mathbf{0} & \mathbf{a}_{j j}^{\mathrm{B}} \mathrm{BR} & \mathbf{0} & \mathbf{a}_{j l}^{\mathrm{BCR}} & \mathbf{0} \\
\mathbf{0} & \mathbf{0} & \mathbf{0} & \mathbf{a}_{k j} & \mathbf{a}_{k k} & \mathbf{a}_{k l} & \mathbf{0} \\
\mathbf{0} & \mathbf{0} & \mathbf{0} & \mathbf{0} & \star & \star & \star
\end{array}\right),
$$

where the new and updated elements are given as

$$
\begin{align*}
& \mathbf{a}_{j j}^{\text {BCR }} \stackrel{\text { def }}{=} \mathbf{a}_{j j}-\ell_{j \mathbf{i}} \mathbf{a}_{i j}-\ell_{j k} \mathbf{a}_{k j},  \tag{39}\\
& \mathbf{a}_{j h}^{\mathrm{BCR}} \stackrel{\text { def }}{=}-\ell_{j i} \mathbf{a}_{i h},  \tag{40}\\
& \mathbf{a}_{j l}^{\text {BCR }} \stackrel{\text { def }}{=}-\ell_{j k} \mathbf{a}_{k l} . \tag{41}
\end{align*}
$$

This process of reduction continues until we are left with only one row, namely $\mathbf{a}_{k k}^{\mathrm{BCR}}$, where $k$ denotes the row/column we finally reduce to. From $\mathbf{a}_{k k}^{\mathrm{BCR}}$, we can determine one block of the inverse via $\mathbf{g}_{k k}=\left(\mathbf{a}_{k k}^{\mathrm{BCR}}\right)^{-1}$. From this single block, the backward recurrence (12) produces all the other blocks of the inverse. The steps are similar to those in Sections 5.1.1 and 5.1.2. The key difference between the Schur phase and BCR is in the pattern of communication in the parallel implementation. The Schur phase is embarrassingly parallel while BCR requires communication at the end of each step.


Fig. 7. Permutation for a $31 \times 31$ block matrix $\mathbf{A}$ (left), which shows that our Schur reduction phase is identical to an unpivoted Gaussian elimination on a suitably permuted matrix PAP (right). Colors are associated with processes. The diagonal block structure of the top left part of the matrix (right panel) shows that this calculation is embarrassingly parallel.


Fig. 8. BCR corresponds to an unpivoted Gaussian elimination of $\mathbf{A}$ with permutation of rows and columns.


Fig. 9. Following a Schur reduction phase on the permuted block matrix PAP, we obtain the reduced block tridiagonal system in the lower right corner (left panel). This reduced system is further permuted to a form shown on the right panel, as was done for BCR in Fig. 8.

## 6. Numerical results

### 6.1. Stability

Our elimination algorithm is equivalent to an unpivoted Gaussian elimination on a suitably permuted matrix PAP for some permutation matrix $\mathbf{P}[19,20]$.

Figs. 7 and 8 show the permutation corresponding to the Schur phase and the cyclic reduction phase for a $31 \times 31$ block matrix $\mathbf{A}$.

In the case of our algorithm, both approaches are combined. The process is shown on Fig. 9. Once the Schur reduction phase has completed on $\mathbf{A}$, we are left with a reduced block tridiagonal system. The rows and columns of this tridiagonal system are then be permuted following the BCR pattern.

Our hybrid method is therefore equivalent to an unpivoted Gaussian elimination. Consequently, the stability of the method is dependent on the stability of using the diagonal blocks of $\mathbf{A}$ as pivots in the elimination process, as is the case for block cyclic reduction.

### 6.2. Load balancing

For the purposes of load balancing, we will assume that blocks in $\mathbf{A}$ are of equal size. Although this is not the case in general, it is still of use for the investigation of nanodevices that tend to be relatively homogeneous and elongated, and thus giving rise to block tridiagonal matrices $\mathbf{A}$ with many diagonal blocks and of relatively identical size. Furthermore, this assumption serves as an introductory investigation on how load balancing should be performed, eventually preparing us for a future investigation of the general case.


Fig. 10. Walltime for our hybrid algorithm and pure BCR for different $\alpha$ s as a basic load balancing parameter. A block tridiagonal matrix $\mathbf{A}$ with $n=512$ diagonal blocks, each of dimension $m=256$, was used for these tests.

There are essentially two sorts of execution profiles for a process: one for corner processes ( $p_{0}$ and $p_{\mathcal{P}-1}$ ) and for central processes. The corner processes perform operations described in Section 5.1.1, while the center processes perform those outlined in 5.1.2.

By performing an operation count under the assumption of equal block sizes throughout $\mathbf{A}$, and assuming an LU factorization cost equal to $\frac{2}{3} d^{3}$ operations and a matrix-matrix multiplication cost of $2 d^{3}$ operations (for a matrix of dimension $d$, cf. [21]), we estimate the ratio $\alpha$ of number of rows for a corner process to number of rows for central processes. An analysis of our algorithm predicts $\alpha=2.636$ to be optimal [22]. For the sake of completeness, we investigate the case of $\alpha=1$, where each process is assigned the same number of rows, while the values $\alpha=2$ and $\alpha=3$ are chosen to bracket the optimal choice.

### 6.3. Benchmarks

The benchmarking of the algorithms presented in this paper was carried out on a Sun Microsystems Sun Fire E25K server. This machine comprises 72 UltraSPARC IV+ dual-core CPUs, yielding a total of 144 CPU cores, each running at 1.8 GHz . Each dual-core CPU had access to 2 MB of shared L2-cache and 32 MB shared L3-cache, with a final layer of 416 GB of RAM.

We estimated that in all probability each participating process in our calculations had exclusive right to a single CPU core. There was no guarantee however that the communication network was limited to handling requests from our benchmarked algorithms. It is in fact highly likely that the network was loaded with other users' applications, which played a role in reducing the performance of our applications. We were also limited to a maximum number of CPU cores of 64 .


Fig. 11. Speedup curves for our hybrid algorithm and pure BCR for different $\alpha$ s. A block tridiagonal matrix $A$ with $n=512$ diagonal blocks, each of dimension $m=256$, was used.


Fig. 12. The total execution time for our hybrid algorithm is plotted against the number of processes $\mathcal{P}$. The choice $\alpha=1$ gives poor results except when the number of processes is large. Other choices for $\alpha$ give similar results. The same matrix $\mathbf{A}$ as before was used.

The execution time was measured for a pure block cyclic reduction algorithm (cf. Algorithm 1) in comparison with our hybrid algorithm (cf. Algorithm 2). The walltime measurements for running these algorithms on a block tridiagonal matrix A with $n=512$ block rows with blocks of dimension $m=256$ is given in Fig. 10 for four different load balancing values $\alpha=\{1,2,2.636,3\}$. The total number of processes used for execution was $\mathcal{P}=\{1,2,4,8,16,32,64\}$ in all cases.

The speedup results corresponding to these walltime measurements are given in Fig. 11 for the four different load balancing values of $\alpha$. For a uniform distribution where $\alpha=1$, a serious performance hit is experienced for $\mathcal{P}=4$. This is due to a poor load balance, as the two corner processes $p_{0}$ and $p_{3}$ terminate their Schur production/reduction phases much sooner than the central processes $p_{1}$ and $p_{2}$. It is then observed that choosing $\alpha$ equal $2,2.636$ or 3 eliminates this dip in speedup. The results appear relatively insensitive to the precise choice of $\alpha$.

It can also be seen that as the number of processes $\mathcal{P}$ increases for a fixed $n$, a greater portion of execution time is attributed to the $B C R$ phase of our algorithm. Ultimately higher communication and computational costs of $B C R$ over the embarrassingly parallel Schur phase dominate, and the speedup curves level off and drop, regardless of $\alpha$.

In an effort to determine which load balancing parameter $\alpha$ is optimal, we compare the walltime execution measurements for our hybrid algorithm in Fig. 12. From this figure, we can conclude that a uniform distribution with $\alpha=1$ leads to poorer execution times; other values of $\alpha$ produce improved but similar results, particularly in the range $\mathcal{P}=4 \ldots 8$, which is a common core count in modern desktop computers.

## 7. Conclusion

We have proposed new recurrence formulas to calculate certain entries of the inverse of a sparse matrix. This problem has application in quantum transport and quantum mechanical models, to calculate Green's and lesser Green's function matrices for example. This calculation scales like $N^{3}$ for a matrix of size $N$ using a naive algorithm. We have shown that the computational cost can be reduced by orders of magnitude using novel recurrences Eqs. (13) and (20). This is an extension of the work of K. Takahashi [11] and others.

A hybrid algorithm for the parallel implementation of these recurrences has been developed that performs well on many processes. This hybrid algorithm combines Schur complement calculations that are embarrassingly parallel with block cyclic reduction. The performance depends on the problem size and the efficiency of the computer cluster network. On a small test case, we observed scalability up to 32 processes. For electronic structure calculations using density functional theory (DFT), this parallel algorithm can be combined with a parallelization over energy points. This will allow running DFT computations for quantum transport with unprecedented problem sizes.

We note that more efficient parallel implementations of cyclic reduction exist. In particular they can reduce the total number of passes in the algorithm. This will be the object of a future paper.

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## Appendix A. Algorithms

This section presents the algorithms of block cyclic reduction and the hybrid method used in this paper. The algorithm InverseBCR given in Algorithm 1 calculates the block tridiagonal portion of the inverse of $\mathbf{A}$, namely $\operatorname{Trid}_{\mathbf{A}}\{\mathbf{G}\}$, via a pure block cyclic reduction ( $B C R$ ) approach. The algorithm performs a $B C R$ reduction on $\mathbf{A}$ on line 2 . This leaves us with a single, final block that can be inverted in order to determine the first block of the inverse, $\mathbf{g}_{k k}$. From here, a call on line 4 takes care of reconstructing the block tridiagonal portion of the inverse $\mathbf{G}$ using this first block of the inverse $\mathbf{g}_{k k}$, and the stored LU factors in $\mathbf{L}$ and $\mathbf{U}$.
Algorithm 1. Inversebcr(A)

```
\(\mathbf{i}^{B C R} \leftarrow\{1,2, \ldots, n\} \quad B C R\) is performed over all of \(\mathbf{A}\)
\(\mathbf{A}, \mathbf{L}, \mathbf{U} \leftarrow \operatorname{ReduceBCR}\left(\mathbf{A}, \mathbf{L}, \mathbf{U}, \mathbf{i}^{\text {BCR }}\right)\)
\(\mathbf{g}_{k k}=\mathbf{a}_{k k}^{-1}\)
\(\mathbf{G} \leftarrow \operatorname{Produce~BCR}\left(\mathbf{A}, \mathbf{L}, \mathbf{U}, \mathbf{G}, \mathbf{i}^{\text {BCR }}\right)\)
return G
```

The algorithm InverseHybrid returns the same result of the block tridiagonal portion of the inverse, but by using the hybrid technique presented in this paper. A significant difference between this algorithm and that of pure BCR is the specification of which row indices $\mathbf{i}^{\mathrm{BCR}}$ the hybrid method should reduce the full block matrix $\mathbf{A}$ to, before performing $B C R$ on this reduced block tridiagonal system.

This variable of $\mathbf{i}^{B C R}$ is explicitly used as an argument in the BCR phases, and implicitly in the Schur phases. The implicit form is manifested through the variables top and bot, that store the value of the top and bottom row indices "owned" by one of the participating parallel processes.
Algorithm 2. InverseHybrid (A, $\mathbf{i}^{\mathrm{BCR}}$ )

```
A, L, \(\mathbf{U} \leftarrow\) ReduceSchur(A)
\(: \mathbf{A}, \mathbf{L}, \mathbf{U} \leftarrow \operatorname{ReduceBCR}\left(\mathbf{A}, \mathbf{L}, \mathbf{U}, \mathbf{i}^{\mathrm{BCR}}\right)\)
\(\mathbf{g}_{k k}=\mathbf{a}_{k k}^{-1}\)
\(\mathbf{G} \leftarrow \operatorname{ProducEBCR}\left(\mathbf{A}, \mathbf{L}, \mathbf{U}, \mathbf{G}, \mathbf{i}^{B C R}\right)\)
\(\mathbf{G} \leftarrow \operatorname{ProduceSchur}(\mathbf{A}, \mathbf{L}, \mathbf{U}, \mathbf{G})\)
return G
```


## A.1. BCR reduction functions

The reduction phase of $B C R$ is achieved through the use of the following two methods. The method REDUCEBCR, given in Algorithm 3, takes a block tridiagonal $\mathbf{A}$ and performs a reduction phase over the supplied indices given in $\mathbf{i}^{\mathrm{BCR}}$. The associated LU factors are then stored appropriately in matrices $\mathbf{L}$ and $\mathbf{U}$.

ReduceBCR loops over each of the levels on line 3, and eliminates the odd-numbered rows given by line 4 . This is accomplished by calls to the basic reduction method Reduce on line 6.

```
Algorithm 3. ReduceBCR(A,L,U,i \(\left.{ }^{\text {BCR }}\right)\)
\(k \leftarrow\) lengthofi \({ }^{\text {BCR }} \quad\) size of the "reduced" system
\(h \leftarrow \log _{2}(k) \quad\) height of the binary elimination tree
for level \(=1\) up to \(h\) do
    \(\mathbf{i}^{\text {elim }} \leftarrow\) determine the active rows
    for row \(=1 \mathrm{up}\) to length of \(\mathbf{i}^{\text {elim }}\) do eliminate odd active rows
        \(\mathbf{A}, \mathbf{L}, \mathbf{U} \leftarrow \operatorname{Reduce}\left(\mathbf{A}, \mathbf{L}, \mathbf{U}\right.\), row, level, \(\left.\mathbf{i}^{\text {elim }}\right)\)
    return \(A, L, U\)
```

The core operation of the reduction phase of BCR are the block row updates performed by the method Reduce given in Algorithm 4, where the full system $\mathbf{A}$ is supplied along with the LU block matrices $\mathbf{L}$ and $\mathbf{U}$. The value row is passed along with $\mathbf{i}^{\text {elim }}$, telling us which row of the block tridiagonal matrix given by the indices in $\mathbf{i}^{\text {elim }}$ we want to reduce towards. The method then looks at neighbouring rows based on the level level of the elimination tree we are currently at, and then performs block row operations with the correct stride.

```
Algorithm 4. Reduce(A,L,U,row,level,ielim \({ }^{\text {elim }}\)
```

```
\(h, i, j, k, l \leftarrow\) get the working indices
```

$h, i, j, k, l \leftarrow$ get the working indices
if $i \geqslant \mathbf{i}^{\text {elim }}[1]$ then $\quad$ if there is a row above
if $i \geqslant \mathbf{i}^{\text {elim }}[1]$ then $\quad$ if there is a row above
$\mathbf{u}_{i j} \leftarrow \mathbf{a}_{i i}^{-1} \mathbf{a}_{i j}^{\mathrm{BCR}}$
$\mathbf{u}_{i j} \leftarrow \mathbf{a}_{i i}^{-1} \mathbf{a}_{i j}^{\mathrm{BCR}}$
$\ell_{j i} \leftarrow \mathbf{a}_{j i} \mathbf{a}_{i i}^{-1}$
$\ell_{j i} \leftarrow \mathbf{a}_{j i} \mathbf{a}_{i i}^{-1}$
$\mathbf{a}_{j j} \leftarrow \mathbf{a}_{j j}-\ell_{j i} \mathbf{a}_{i j}$
$\mathbf{a}_{j j} \leftarrow \mathbf{a}_{j j}-\ell_{j i} \mathbf{a}_{i j}$
if $\mathbf{a}_{i h}$ exists then if the row above is not the "top" row
if $\mathbf{a}_{i h}$ exists then if the row above is not the "top" row
$\mathbf{a}_{j h} \leftarrow-\ell_{j i} \mathbf{a}_{i h}$
$\mathbf{a}_{j h} \leftarrow-\ell_{j i} \mathbf{a}_{i h}$
if $k \leqslant \mathbf{i}^{\text {elim }}[$ end $]$ then if there is a row below
if $k \leqslant \mathbf{i}^{\text {elim }}[$ end $]$ then if there is a row below
$\mathbf{u}_{k j} \leftarrow \mathbf{a}_{k k}^{-1} \mathbf{a}_{k j}$
$\mathbf{u}_{k j} \leftarrow \mathbf{a}_{k k}^{-1} \mathbf{a}_{k j}$
$\ell_{j k} \leftarrow \mathbf{a}_{j k} \mathbf{a}_{k k}^{-1}$
$\ell_{j k} \leftarrow \mathbf{a}_{j k} \mathbf{a}_{k k}^{-1}$
$\mathbf{a}_{j j} \leftarrow \mathbf{a}_{j j}-\ell_{j k} \mathbf{a}_{k j}$
$\mathbf{a}_{j j} \leftarrow \mathbf{a}_{j j}-\ell_{j k} \mathbf{a}_{k j}$
if $\mathbf{a}_{k l}$ exists then if the row below is not the "bottom" row
if $\mathbf{a}_{k l}$ exists then if the row below is not the "bottom" row
$\mathbf{a}_{j l} \leftarrow-\ell_{j k} \mathbf{a}_{k l}$
$\mathbf{a}_{j l} \leftarrow-\ell_{j k} \mathbf{a}_{k l}$
return A,L,U

```
    return A,L,U
```


## A.2. BCR production functions

The production phase of BCR is performed via a call to ProduceBCR given in Algorithm 5. The algorithm takes as input an updated matrix $\mathbf{A}$, associated LU factors, an inverse matrix $\mathbf{G}$ initialized with the first block inverse $\mathbf{g}_{k k}$, and a vector of indices $\mathbf{i}^{\mathrm{BCR}}$ defining the rows/columns of $\mathbf{A}$ on which $B C R$ is to be performed.

The algorithm works by traversing each level of the elimination tree (line 1), where an appropriate striding length is determined and an array of indices $\mathbf{i}^{\text {prod }}$ is generated. This array is a subset of the overall array of indices $\mathbf{i}^{\text {BCR }}$, and is determined by considering which blocks of the inverse have been computed so far. The rest of the algorithm then works through each of these production indices $\mathbf{i}^{\text {prod }}$, and calls the auxiliary methods CornerProduce and CenterProduce.
Algorithm 5. ProduceBCR(A,L,U,G,ii ${ }^{\text {BCR }}$ )

```
for level \(=h\) down to 1 do
    stride \(\leftarrow 2^{\text {level }}-1\)
    \(\mathbf{i}^{\text {prod }} \leftarrow\) determine the rows to be produced
    for \(i=1\) up to length of \(\mathbf{i}^{\text {prod }}\) do
        \(k_{\mathrm{to}} \leftarrow \mathbf{i}^{\mathrm{BCR}}\left[\mathbf{i}^{\mathrm{prod}}[i]\right]\)
        if \(i=1\) then
            \(k_{\text {from }} \leftarrow \mathbf{i}^{\mathrm{BCR}}\left[\mathbf{i}^{\text {prod }}[i]+\right.\) stride \(]\)
            \(\mathbf{G} \leftarrow\) CornerProduce \(\left(\mathbf{A}, \mathbf{L}, \mathbf{U}, \mathbf{G}, \mathrm{k}_{\text {from }}, \mathbf{k}_{\text {to }}\right.\) )
        if \(i \neq 1\) and \(i=\) length of \(i^{\text {prod }}\) then
            if \({ }^{\text {prod }}[\) end \(] \leqslant\) length of \(\mathbf{i}^{\mathrm{BCR}}-\) stride then
                    \(k_{\text {above }} \leftarrow \mathbf{i}^{\mathrm{BCR}}\left[\mathbf{i}^{\text {prod }}[i]-\right.\) stride \(]\)
                    \(k_{\text {below }} \leftarrow \mathbf{i}^{\mathrm{BRR}}\left[\mathbf{i}^{\text {prod }}[i]+\right.\) stride \(]\)
                    \(\mathbf{G} \leftarrow \operatorname{Center} \operatorname{Produce}\left(\mathbf{A}, \mathbf{L}, \mathbf{U}, \mathbf{G}, \mathrm{k}_{\text {above }}, \mathrm{k}_{\mathrm{to}}, \mathrm{k}_{\text {below }}\right)\)
                else
                    \(k_{\text {from }} \leftarrow \mathbf{i}^{\mathrm{BCR}}\left[\mathbf{i}^{\text {prod }}[i]-\right.\) stride \(]\)
                    \(\mathbf{G} \leftarrow \operatorname{CornerProduce}\left(\mathbf{A}, \mathbf{L}, \mathbf{U}, \mathbf{G}, k_{\text {from }}, k_{\text {to }}\right)\)
        if \(i \neq 1\) and \(i \neq\) length of \(\mathbf{i}^{\text {prod }}\) then
            \(k_{\text {above }} \leftarrow \mathbf{i}^{\text {BCR }}\left[\mathbf{i}^{\text {prod }}[i]-\right.\) stride \(]\)
            \(k_{\text {below }} \leftarrow \mathbf{i}^{\mathrm{BCR}}\left[\mathbf{i}^{\text {prod }}[i]+\right.\) stride \(]\)
            \(\mathbf{G} \leftarrow \operatorname{CenterProduce}\left(\mathbf{A}, \mathbf{L}, \mathbf{U}, \mathbf{G}, k_{\text {above }}, k_{\mathrm{to}}, k_{\text {below }}\right)\)
```

    return \(\mathbf{G}\)
    The auxiliary methods CornerProduce and CenterProduce are given in Algorithms 6 and 7.

```
Algorithm 6. CornerProduce(A,L,U,G, \(k_{\text {from }}, k_{\text {to }}\) )
```

```
\mp@subsup{g}{\mp@subsup{k}{\mathrm{ from }}{\prime},\mp@subsup{k}{\mathrm{ to }}{}}{}\leftarrow-\mp@subsup{\mathbf{g}}{\mp@subsup{k}{\mathrm{ from }}{\prime},\mp@subsup{k}{\mathrm{ from }}{}}{}\mp@subsup{\ell}{\mp@subsup{k}{\mathrm{ from }}{\prime},\mp@subsup{k}{\mathrm{ to }}{}}{}
```




```
return G
```

Algorithm 7. CenterProduce (A,L, U, G, $\left.k_{\text {above }}, k_{\text {to }}, k_{\text {below }}\right)$

|  |
| :---: |
|  |  |
|  |  |
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## A.3. Hybrid auxiliary functions

Finally, this subsection deals with the auxiliary algorithms introduced by our hybrid method. Prior to any BCR operation, the hybrid method applies a Schur reduction to $\mathbf{A}$ in order to reduce it to a smaller block tridiagonal system. This reduction is handled by Reduceschur given in Algorithm 8, while the final production phase to generate the final block tridiagonal $\operatorname{Trid}_{\mathbf{A}}\{\mathbf{G}\}$ is done by Produceschur in Algorithm 9.

```
Algorithm 8. ReduceSchur(A)
if myPID \(=0\) and \(\mathcal{P}>1\) then corner eliminate downwards
    for \(i=t o p+1\) up to bot do
        \(\ell_{i, i-1} \leftarrow \mathbf{a}_{i, i-1} \mathbf{a}_{i-1, i-1}^{-1}\)
        \(\mathbf{u}_{i-1, i} \leftarrow \mathbf{a}_{i-1, i-1}^{-1} \mathbf{a}_{i-1, i}\)
        \(\mathbf{a}_{i i} \leftarrow \mathbf{a}_{i i}-\ell_{i, i-1} \mathbf{a}_{i-1, i}\)
if myPID \(=\mathcal{P}-1\) then corner eliminate upwards
    for \(i=\) bot -1 down to top do
        \(\ell_{i, i+1} \leftarrow \mathbf{a}_{i, i+1} \mathbf{a}_{i+1, i+1}^{-1}\)
        \(\mathbf{u}_{i+1, i} \leftarrow \mathbf{a}_{i+1, i+1}^{-1} \mathbf{a}_{i+1, i}\)
            \(\mathbf{a}_{i i} \leftarrow \mathbf{a}_{i i}-\ell_{i, i+1} \mathbf{a}_{i+1, i}\)
    if myPID \(\neq 0\) and myPID \(\neq \mathcal{P}-1\) and \(\mathcal{P}>1\) then center elim. down
        for \(i=t o p+2\) down to bot do
            \(\ell_{i, i-1} \leftarrow \mathbf{a}_{i, i-1} \mathbf{a}_{i-1, i-1}^{-1}\)
            \(\ell_{\text {top }, i-1} \leftarrow \mathbf{a}_{\text {top }, i-1} \mathbf{a}_{i-1, i-1}^{-1}\)
            \(\mathbf{u}_{i-1, i} \leftarrow \mathbf{a}_{i-1, i-1}^{-1} \mathbf{a}_{i-1, i}\)
            \(\mathbf{u}_{i-1, \text { top }} \leftarrow \mathbf{a}_{i-1, i-1}^{-1} \mathbf{a}_{i-1, \text { top }}\)
            \(\mathbf{a}_{i i} \leftarrow \mathbf{a}_{i i}-\ell_{i, i-1} \mathbf{a}_{i-1, i}\)
            \(\mathbf{a}_{\text {top,top }} \leftarrow \mathbf{a}_{\text {top,top }}-\ell_{\text {top }, i-1} \mathbf{a}_{\text {i-1,top }}\)
            \(\mathbf{a}_{i, \text { top }} \leftarrow-\ell_{i, i-1} \mathbf{a}_{i-1, \text { top }}\)
            \(\mathbf{a}_{\text {top }, i} \leftarrow-\ell_{\text {top }, i-1} \mathbf{a}_{i-1, i}\)
    return A,L,U
```

The Schur reduction algorithm takes the full initial block tridiagonal matrix $\mathbf{A}$, and through the implicit knowledge of how the row elements of $\mathbf{A}$ have been assigned to processes, proceeds to reduce $\mathbf{A}$ into a smaller block tridiagonal system. This implicit knowledge is provided by the top and bot variables, which specify the topmost and bottommost row indices for this process.

The reduction Algorithm 8 is then split into three cases. If the process owns the topmost rows of $\mathbf{A}$, it performs a corner elimination downwards. If it owns the bottommost rows, it then performs a similar operation, but in an upwards manner. Finally, if it owns rows that reside in the middle of $\mathbf{A}$, it performs a center reduction operation.

Finally, once the hybrid method has performed a full reduction and a subsequent BCR portion of production, the algorithm ProduceSchur handles the production of the remaining elements of the inverse $\mathbf{G}$.
Algorithm 9. ProduceSchur(A,L,U,G)

```
if myPID = 0 and P}>1\mathrm{ then corner produce upwards
    for i= bot down to top +1 do
        \mp@subsup{\mathbf{g}}{i,i-1}{}}\leftarrow-\mp@subsup{\mathbf{g}}{ii}{}\mp@subsup{\ell}{i,i-1}{
        \mp@subsup{\mathbf{g}}{i-1,i}{}\leftarrow-\mp@subsup{\mathbf{u}}{i-1,i}{}\mp@subsup{\mathbf{g}}{ii}{}
        \mp@subsup{\mathbf{g}}{i-1,i-1}{\prime}}\leftarrow\mp@subsup{\mathbf{a}}{i-1,i-1}{-1}-\mp@subsup{\mathbf{u}}{i-1,i}{}\mp@subsup{\mathbf{g}}{i,i-1}{
if myPID =\mathcal{P}-1\mathrm{ then corner produce downwards}
    for }i=top up to bot - 1 do
```

```
    \(\mathbf{g}_{i, i+1} \leftarrow-\mathbf{g}_{i i} \ell_{i, i+1}\)
    \(\mathbf{g}_{i+1, i} \leftarrow-\mathbf{u}_{i+1, i} \mathbf{g}_{i i}\)
    \(\mathbf{g}_{i+1, i+1} \leftarrow \mathbf{a}_{i+1, i+1}^{-1}-\mathbf{u}_{i+1, i} \mathbf{g}_{i, i+1}\)
if myPID \(\neq 0\) and myPID \(\neq \mathcal{P}-1\) and \(\mathcal{P}>1\) then center produce up
    \(\mathbf{g}_{\text {bot }, \text { bot }-1} \leftarrow-\mathbf{g}_{\text {bot,top }} \ell_{t o p, b o t-1}-\mathbf{g}_{\text {bot }, \text { bot }} \ell_{\text {bot,bot-1 }}\)
    \(\mathbf{g}_{\text {bot }-1, \text { bot }} \leftarrow-\mathbf{u}_{\text {bot }-1, \text { bot }} \mathbf{g}_{\text {bot }, \text { bot }}-\mathbf{u}_{\text {bot }-1, \text { top }} \mathbf{g}_{\text {top }, \text { bot }}\)
    for \(i=b o t-1 \mathbf{u p}\) to \(t o p+1\) do
        \(\mathbf{g}_{\text {top }, i} \leftarrow-\mathbf{g}_{\text {top }, \text { top }} \ell_{\text {top }, i}-\mathbf{g}_{\text {top }, i+1} \ell_{i+1, i}\)
    \(\mathbf{g}_{i, \text { top }} \leftarrow-\mathbf{u}_{i, i+1} \mathbf{g}_{i+1, \text { top }}-\mathbf{u}_{i, \text { top }} \mathbf{g}_{\text {top }, \text { top }}\)
    for \(i=b o t-1\) up to \(t o p+2\) do
        \(\mathbf{g}_{i i} \leftarrow \mathbf{a}_{i i}^{-1}-\mathbf{u}_{i, t o p} \mathbf{g}_{\text {top }, i}-\mathbf{u}_{i, i+1} \mathbf{g}_{i+1, i}\)
        \(\mathbf{g}_{i-1, i} \leftarrow-\mathbf{u}_{i-1, \text { top }} \mathbf{g}_{\text {top }, i}-\mathbf{u}_{i-1, i} \mathbf{g}_{i i}\)
    \(\mathbf{g}_{i, i-1} \leftarrow-\mathbf{g}_{i, \text { top }} \ell_{\text {top }, i-1}-\mathbf{g}_{i i} \ell_{i, i-1}\)
    \(\mathbf{g}_{\text {top }+1, \text { top }+1} \leftarrow \mathbf{a}_{\text {top }+1, \text { top }+1}^{-1}-\mathbf{u}_{\text {top }+1, \text { top }} \mathbf{g}_{\text {top }, \text { top }+1}-\mathbf{u}_{\text {top }+1, \text { top }+2} \mathbf{g}_{\text {top }+2, \text { top }+1}\)
return G
```


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[^1]:    ${ }^{1}$ The method of cyclic reduction was first proposed by Schröder in 1954 and published in German [15].

